



### TWR-S12G64

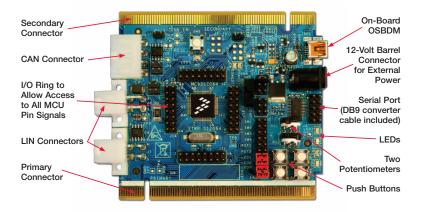
Scalable Platform for Automotive Applications





Quick Start Guide TOWER SYSTEM

### Get to Know the TWR-S12G64





#### TWR-S12G64

### Freescale Tower System

The TWR-S12G64 module is a single board computer as well as part of the Freescale Tower System, a modular development platform that enables rapid prototyping and tool re-use through reconfigurable hardware. Elevate your design to the next level and begin constructing your Tower System today.

### TWR-S12G64 Features

- S12G64 microcontroller (48-pin LQFP)
- On-board JTAG connection via open source OSBDM circuit using the MPC9S08JM microcontroller
- Visit **pemicro.com/osbdm** for source code
- High-speed CAN interface
- I IN interface
- Potentiometer with LP filter
- LED indicators
- RS-232 serial communication interface

Quick Start Guide TOWER SYSTEM

### Step-by-Step Installation Instructions

In this quick start guide, you will learn how to set up the TWR-S12G64 board and run the default exercise.

## Install Software

- Install CodeWarrior Development Studio for S12 V5.1 or later
- Install CodeWarrior service pack for S12G64
- Install CodeWarrior service pack for S12G Processor Expert support

A 30-day evaluation license of CodeWarrior is included on the DVD for your convenience. For updates, please visit freescale.com/TWR-S12G64.

### 2 Connect the USB Cable

Connect one end of the USB cable to the PC and the other end to the mini-B connector on the TWR-S12G64 board. Allow the PC to automatically configure the USB drivers if needed. Verify JP5 has a jumper installed to connect three and four pins.

# 3 Connect the UART Cable

Connect DB9 cable adapter to J5 and attach to PC's serial port (requires HyperTerminal to monitor TWR-S12G64 demo communications: 9600 BR, 8-bit, no parity, one stop bit, no HW control), verify two jumpers are installed at JP2 connecting 3-5 and 4-6. Verify 10 jumpers are installed at JP1 connecting 1-2, 3-4, 5-6, 7-8, 9-10, 11-12, 13-14, 15-16, 17-18 and 19-20.

# Using the Example Project

The pre-loaded example project utilizes the TWR-S12G64's potentiometer 1, push button switches and LEDs. Once the board is plugged in, you can adjust potentiometer 1 and the bank of four LEDs should increase/decrease the toggling speed in response. Each LED will be turned off while the corresponding push button is pressed. The value of the RV1 value can be monitored in the HyperTerminal window.

#### 5 Learn More About the S12G64

Read the release notes and documentation on the DVD or at freescale.com/S12G.

- The Processor Expert graphical initialization software included in your CodeWarrior installation will help reduce your time to market
- CodeWarrior for S12 with examples

\_\_\_\_\_ 5

Quick Start Guide TOWER SYSTEM

### TWR-S12G64 Jumper Options

The following is a list of all jumper options. The default installed jumper settings are shown in white text within the blue boxes.

Jumper	Option	Setting	Description
JP1	USER SWs, RVs and LED Selection	1-2	Connect PAD4 pin to SW1
		3-4	Connect PAD5 pin to SW2
		5-6	Connect PAD6 pin to SW3
		7-8	Connect PAD7 pin to SW4
		9-10	Connect PAD10 to RV1 POT1
		11-12	Connect PAD11 to RV2 POT2
		13-14	Connect PP0 pin to LED1
		15-16	Connect PP1 pin to LED2
		17-18	Connect PP2 pin to LED3
		19-20	Connect PP3 pin to LED4
JP2	LIN TX Enable	1-3	Routes SCI (TXD0) signal to be output by LIN transceiver, chip (U4) at J6 and J7
	LIN RX Enable	2-4	Routes SCI (RXD0) signal to be output by LIN transceiver, chip (U4) at J6 and J7
	UART TX Enable	3-5	Routes SCI (TXD0) signal to be output by RS232 chip (U3) at J5
	UART RX Enable	4-6	Routes SCI (RXD0) signal to be output by RS232 chip (U3) at J5

TWR-S12G64 Jumper Options (continued)

Jumper	Option	Setting	Description
JP3	LIN POWER	1-2	12 volts for LIN bus
		3-4	12 volts for LIN transceiver
JP4	CAN PWR	1-2	CAN tranceiver power enable
JP5	PWR Selector	1-2	Selects the board to be powered from the 3.3-volt elevator card rail
		3-4	Selects the board to be powered from the 5-volt USB connector
		5-6	External source selected as power source
JP6	CAN (H/L)	1-2	SPLIT Termination to CAN - H line
		3-4	SPLIT Termination to CAN - L line

\_\_\_\_\_\_ 7

#### **Quick Start Guide**

Visit **freescale.com/TWR-S12G64** for information on the TWR-S12G64, including:

- TWR-S12G64 quick start guide
- TWR-S12G64 board schematics

## For more information, visit freescale.com/Tower Join the online Tower community at towergeeks.org

Freescale, the Freescale logo and CodeWarrior are trademarks or registered trademarks of Freescale Semiconductor, Inc. Reg. U.S. Pat. & Tm. Off. Processor Expert is a trademark of Freescale Semiconductor, Inc. All other product or service names are the property of their respective owners. © 2011 Freescale Semiconductor, Inc.

freescale

Doc Number: TWRS12G64QSG REV 0 Agile Number: 926-78643 REV A